

	TD-LTE	Cat4 TD-LTE 117/30Mbps
	FDD-LTE	Cat4 FDD-LTE 150/50Mbps
	DC-HSPA+	42/11.2Mbps
	TD-HSPA	2.8/2.3Mbps
	EVDO Rev.A	3.1/1.8Mbps
	EDGE	Class12, 236.8kbps/236.8kbps
	GPRS	Class12, 85.6kbps/85.6kbps
SIM	DSDA 3.0/1.8V SIM LW/G+G with CSFB to W/G L/TDS/G+G with CSFB to TDS/G L/EVDO/CDMA1x+CDMA1x	
Display	Matrix: HD 1280*800 60fps; FULL HD: 1920*1200 45fps	
	LCM Size: User defined	
	Interface: MIPI DSI 4-lane	
		Interface: main: MIPI CSI 4-lane; front: MIPI CSI 4-lane
Camera	P14(i)-17(2) 51764550145 30.954418443 25.71082 9 Tf11 0 0 1 20	

	PIN	LTE LTE GPS 2.4G WiFi-
	Audio	1 MIC 1 MIC 1 0.8W D 1 1

3.

SLM757-QE	TDD-LTE: B38/40/41 FDD-LTE: B1/3/5/7/8/20 WCDMA: B1/B5/B8 GSM: B5/3/8
SLM757-QA	FDD-LTE B2/4/5/12/13/17 WCDMA B2/4/5/8

4.SLM757

SLM757 238pin

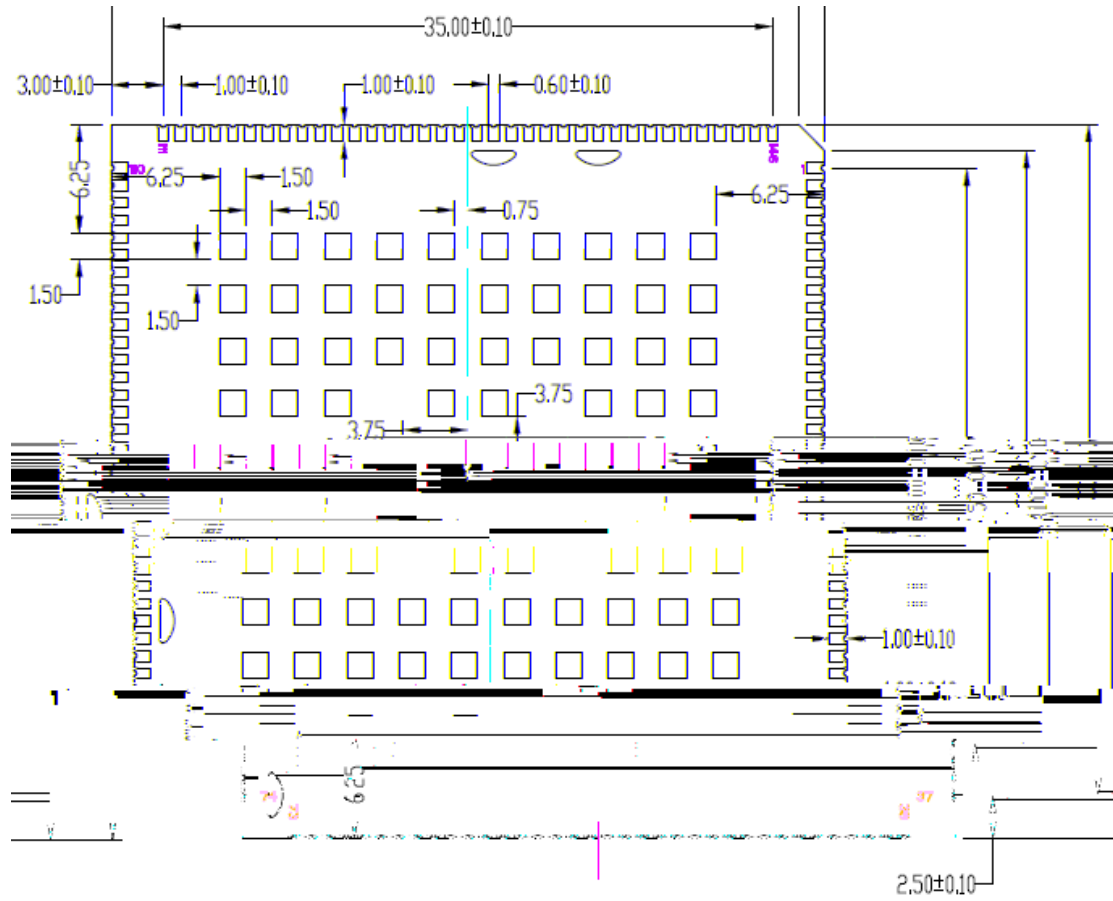
PIN#	SLM757 Pin name	Pad characteristics	Functional description
1	GND	GND	GND
2	VBUS	PI,PO	USB Voltage

66	MIPI_DSI0_CLK_P	AO	MIPI display serial interface 0 clock+
67	MIPI_DSI0_LANE1_N	AO	MIPI display serial interface 0 lane1-
68	MIPI_DSI0_LANE1_P	AO	MIPI display serial interface 0 lane1+
69	MIPI_DSI0_LANE3_N	AO	MIPI display serial interface 0 lane3-
70	MIPI_DSI0_LANE3_P	AO	MIPI display serial interface 0 lane3+
71	MIPI_DSI0_LANE2_N	AO	MIPI display serial interface 0 lane2-
72	MIPI_DSI0_LANE2_P	AO	MIPI display serial interface 0 lane2+
73	GND	GND	GND
74	RF_WIFI/BT	AI	RF signal for WIFI/BT
75	GND	GND	GND
76	GPIO5_UART0_RX	B-PD:nppukp	Configurable I/O, UART0 RX
77	GPIO4_UART0_TX	B-PD:nppukp	Configurable I/O, UART0 TX
78	GND	GND	GND
79	GPIO65_TP_INT_N	B-PD:nppukp	Configurable I/O, TP INT
80	GPIO64_TP_RST_N	B-PD:nppukp	Configurable I/O, TP RESET
81	GPIO11_TP_I2C3_SCL	B-PD:nppukp	Configurable I/O, TP I2C SCL
82	GPIO10_TP_I2C3_SDA	B-PD:nppukp	Configurable I/O, TP I2C SDA
83	GND	GND	GND
84	RF_5G	AI	RF signal for WIFI 5G
85	GND	GND	GND
86	GPIO61_LCD_ID	B-PD:nppukp	Configurable I/O, primary LCD ID
87	GPIO60_LCD_RST_N	B-PD:nppukp	Configurable I/O, primary LCD RESET
88	GPIO24_LCD_TE0	B-PD:nppukp	Configurable I/O, primary LCD TE
89	GND	GND	GND
90	RF_MAIN	AI	RF signal for main ANT
91	GND	GND	GND
92	GPIO15_SENSOR_I2C4_SCL	B-PD:nppukp	Configurable I/O, SENSOR I2C SCL
93	GPIO14_SENSOR_I2C4_SDA	B-PD:nppukp	Configurable I/O, SENSOR I2C SDA
94	GPIO7_I2C2_SCL	B-PD:nppukp	Configurable I/O, I2C SCL or UART0 RTS
95	GPIO6_I2C2_SDA	B-PD:nppukp	Configurable I/O, I2C SDA or UART0 CTS
96	UIM2_DET	B-PD:nppukp	Configurable I/O, UIM2 removal detection
97	UIM2_RESET	B-PD:nppukp	Configurable I/O, UIM2 reset
98	UIM2_CLK	B-PD:nppukp	Configurable I/O, UIM2 clock
99	UIM2_DATA	B-PD:nppukp	Configurable I/O, UIM2 data
100	VREG_L15_UIM2	PO	PMIC output for UIM2
101	UIM1_DET	B-PD:nppukp	Configurable I/O, UIM1 removal detection

102	UIM1_RESET	B-PD:nppukp	Configurable I/O,UIM1 reset
103	UIM1_CLK	B-PD:nppukp	Configurable I/O,UIM1 clock
104	UIM1_DATA	B-PD:nppukp	Configurable I/O,UIM1 data
105	VREG_L14_UIM1	PO	PMIC output for UIM1

138	USB_ID	AI	USB ID
139	USB_DM	AI,AO	USB data minus
140	USB_DP	AI,AO	USB data plus
141	GND	GND	GND
142	GND	GND	GND
143	VBAT	PI,PO	Battery
144	VBAT	PI,PO	Battery
145	VBAT	PI,PO	Battery
146	GND	GND	GND
147	GND	GND	GND
148	GND	GND	GND
149	LCD_CABC		

2D



6. PIN MAP

